

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,162,214 B2
APPLICATION NO. : 10/053403
DATED : January 9, 2007
INVENTOR(S) : Eichin et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1,

Line 52, after "and", replace "Via" by --via--;

Column 2,

Line 42, after "particular", insert --.--;

Column 3,

Line 34, after "<-relation", replace "or>-relation" by --or >-relation--;

Line 41, after "preferably the", replace "capacitors" by --capacitor,--;

Column 4,

Line 8, after "FIG.", replace "2;" by --2:--;

Line 11, after "the", replace "shutdown" by --shut-down--;

Line 23, after "signals", replace "SIN" by --S_{IN}--;

Line 38, after "signal", replace "S_{out}" by --S_{OUT}--;

Line 49, after "signal", replace "SIN" by --S_{IN}--;

Line 51, after "digital-analog-converter", replace "(DAG)" by --(DAC)--;

Line 56, after "reduced", insert --.--;

Line 61, after "signal", replace "SIN" by --S_{IN}--;

Column 5,

Line 13, after "source", replace "VS" by --V_S--;

Line 60, after "FIG. 3", insert --.--;

Column 6,

Line 3, after "inverting", replace "input.A t-V_C" by --input. A t-V_C--;

Line 10, before "of", replace "SDout" by --SD_{out}--;

after "t-SD_{out}", delete --.--;

Line 39, after "time", replace "to" by --t₀--;

Line 42, after "voltage", replace "Vs" by --V_S--;

Line 44, after "not", delete "10";

Line 52, after "D_{out}", insert --.--;

Line 62, after "source", replace "Q₁" by --Q₁--;

Line 67, after "time", replace "t3" by --t₃--;

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 7,


Line 12, before "from replace "Dot_{out}" by --D_{out}--;
Line 18, after "signal", replace "SD_{pout}" by --SD_{out}--;
Line 35, after "voltage", replace "Vs" by --V_s--;
Line 66, after "or", replace "cut," by --out,--;

Column 8,

Line 3, after "time", replace "t_{1c}" by --t₁₀--;
Line 8, after "attained", delete --,--;
Line 10, after "SD_{out}", delete --,--;
Line 51, after "value", insert --,--.

Signed and Sealed this

Nineteenth Day of June, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office